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### 1 [Fault tolerance overhead in network-on-chip flow control schemes](#)

 Antonio Pullini, Federico Angiolini, Davide Bertozzi, Luca Benini  
 September 2005 SBCCI '05: Proceedings of the 18th annual symposium on Integrated circuits and system design

Publisher: ACM

 Full text available: [Pdf](#) (362.21 KB) [Additional Information: full citation, abstract, references, cited by, index terms](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 78, Citation Count: 4

Flow control mechanisms in Network-on-Chip (NoC) architectures are critical for fast packet propagation across the network and for low idling of network resources. Buffer management and allocation are fundamental tasks of each flow control scheme. Buffered ...

Keywords: error correction, fault tolerance, flow control, network on chip

### 2 [Recovery guarantees for Internet applications](#)

 Roger Barga, David Lomet, German Shegalov, Gerhard Weikum  
 August 2004 ACM Transactions on Internet Technology (TOIT), Volume 4 Issue 3  
 Publisher: ACM

 Full text available: [Pdf](#) (997.52 KB) [Additional Information: full citation, abstract, references, cited by, index terms, review](#)

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 234, Citation Count: 3

Internet-based e-services require application developers to deal explicitly with failures of the underlying software components, for example web servers, servlets, browser sessions, and so forth. This complicates application programming, and may expose ...

Keywords: Exactly-once execution, application recovery, communication protocols, interaction contracts

### 3 [Congestion control for ABR traffic in an ATM network](#)

 Anna Hać, Hong Lin  
 August 1999 International Journal of Network Management, Volume 9 Issue 4  
 Publisher: John Wiley & Sons, Inc.

 Full text available: [Pdf](#) (295.89 KB) [Additional Information: full citation, abstract, references, index terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 33, Citation Count: 0

This article presents a novel approach to ATM congestion control for ABR &par; Allowed Bit Rate &par; service. This approach can achieve a desired fair link utilization using adaptive filters to prevent both network collapse and the 'beat down' ...

#### 4 [A rate-based congestion control scheme for ABR service in ATM networks](#)

Anna Hać, Yingjun Ma

October 1998 International Journal of Network Management, Volume 8 Issue 5

Publisher: John Wiley & Sons, Inc.

Full text available:  Pdf (505.39 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 14, Citation Count: 0

In this article we describe an improved rate-based congestion control scheme for Available Bit Rate &par; ABR &par; service in ATM networks. The analytical results are presented and the characteristics of the designed scheme are illustrated by using ...

#### 5 [Traffic descriptor mapping and traffic control for frame relay over ATM network](#)

Sudhir S. Dixit, Sharad Kumar

February 1998 IEEE/ ACM Transactions on Networking (TON), Volume 6 Issue 1

Publisher: IEEE Press

Full text available:  Pdf (345.04 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 44, Citation Count: 0

Keywords: ATM, cell relay, frame relay, quality of service, traffic management

#### 6 [Flash crowd mitigation via adaptive admission control based on application-level observations](#)

Xuan Chen, John Heidemann

August 2005 ACM Transactions on Internet Technology (TOIT), Volume 5 Issue 3

Publisher: ACM

Full text available:  Pdf (979.43 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 90, Citation Count: 2

We design an adaptive admission control mechanism, *network early warning system* (NEWS), to protect servers and networks from flash crowds and maintain high performance for end-users. NEWS detects flash crowds from performance degradation in responses ...


Keywords: Flash crowds, admission control, experimentation with testbeds, simulations

#### 7 [Improving the reliability of commodity operating systems](#)

Michael M. Swift, Brian N. Bershad, Henry M. Levy

February 2005 ACM Transactions on Computer Systems (TOCS), Volume 23 Issue 1

Publisher: ACM

Full text available:  Pdf (459.98 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 21, Downloads (12 Months): 340, Citation Count: 13

Despite decades of research in extensible operating system technology, extensions such as device drivers remain a significant cause of system failures. In Windows XP, for example, drivers account for 85&percent; of recently reported failures. This article ...

**Keywords:** I/O, Recovery, device drivers, protection, virtual memory

## 8 Recovering device drivers



Michael M. Swift, Muthukaruppan Annamalai, Brian N. Bershad, Henry M. Levy  
November 2006 ACM Transactions on Computer Systems (TOCS), Volume 24 Issue 4  
Publisher: ACM

Full text available: Pdf (365.93 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 171, Citation Count: 1

This article presents a new mechanism that enables applications to run correctly when device drivers fail. Because device drivers are the principal failing component in most systems, reducing driver-induced failures greatly improves overall reliability. ...

**Keywords:** I/O, Recovery, device drivers

## 9 One more bit is enough



Yong Xia, Lakshminarayanan Subramanian, Ion Stoica, Shivkumar Kalyanaraman  
October 2005 ACM SIGCOMM Computer Communication Review, Volume 35 Issue 4  
Publisher: ACM

Full text available: Pdf (1.19 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 86, Citation Count: 5

Achieving efficient and fair bandwidth allocation while minimizing packet loss in high bandwidth-delay product networks has long been a daunting challenge. Existing end-to-end congestion control (eg TCP) and traditional congestion notification ...

**Keywords:** AQM, ECN, TCP, XCP, congestion control, protocol

## 10 Instruction packing: Toward fast and energy-efficient instruction scheduling



Joseph J. Sharkey, Dmitry V. Ponomarev, Kanad Ghose, Oguz Ergin  
June 2006 ACM Transactions on Architecture and Code Optimization (TACO), Volume 3 Issue 2  
Publisher: ACM

Full text available: Pdf (665.64 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 73, Citation Count: 0

Traditional dynamic scheduler designs use one issue queue entry per instruction, regardless of the actual number of operands actively involved in the wakeup process. We propose Instruction Packing---a novel microarchitectural technique that reduces both ...

**Keywords:** Issue queue, instruction packing, low power

## 11 One more bit is enough



Yong Xia, Lakshminarayanan Subramanian, Ion Stoica, Shivkumar Kalyanaraman  
August 2005 SIGCOMM '05: Proceedings of the 2005 conference on Applications, technologies, architectures, and protocols for computer communications

Publisher: ACM

Full text available:  Pdf (1.19 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 86, Citation Count: 5

Achieving efficient and fair bandwidth allocation while minimizing packet loss in high bandwidth-delay product networks has long been a daunting challenge. Existing end-to-end congestion control (*eg* TCP) and traditional congestion notification ...

Keywords: AQM, ECN, TCP, XCP, congestion control, protocol

## 12 Backtracking intrusions



Samuel T. King, Peter M. Chen

February 2005 ACM Transactions on Computer Systems (TOCS), Volume 23 Issue 1

Publisher: ACM

Full text available:  Pdf (647.38 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 191, Citation Count: 3

Analyzing intrusions today is an arduous, largely manual task because system administrators lack the information and tools needed to understand easily the sequence of steps that occurred in an attack. The goal of BackTracker is to identify automatically ...

Keywords: Computer forensics, information flow, intrusion analysis

## 13 Slipstream processors: improving both performance and fault tolerance



Karthik Sundaramoorthy, Zach Purser, Eric Rotenberg

December 2000 ACM SIGARCH Computer Architecture News, Volume 28 Issue 5

Publisher: ACM

Full text available:  Pdf (111.54 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 55, Citation Count: 44

Processors execute the full dynamic instruction stream to arrive at the final output of a program, yet there exist shorter instruction streams that produce the same overall effect. We propose creating a shorter but otherwise equivalent version of the ...

## 14 Charles W. Bachman interview: September 25-26, 2004; Tucson, Arizona



Thomas Haigh

January 2006 ACM Oral History interviews

Publisher: ACM

Full text available:  Pdf (974.87 KB)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 45, Downloads (12 Months): 489, Citation Count: 0

Charles W. Bachman reviews his career. Born during 1924 in Kansas, Bachman attended high school in East Lansing, Michigan before joining the Army Anti Aircraft Artillery Corp, with which he spent two years in the Southwest Pacific Theater, during ...

## 15

[Slipstream processors: improving both performance and fault tolerance](#)



Karthik Sundaramoorthy, Zach Purser, Eric Rotenberg  
November 2000 ACM SIGPLAN Notices, Volume 35 Issue 11  
Publisher: ACM

Full text available: Pdf (1.51 MB)

Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 40, Citation Count: 1

Processors execute the full dynamic instruction stream to arrive at the final output of a program, yet there exist shorter instruction streams that produce the same overall effect. We propose creating a shorter but otherwise equivalent version of the ...

## 16 Design, analysis, and implementation of DVSR: a fair high-performance protocol for packet rings

Violeta Gambiroza, Ping Yuan, Laura Balzano, Yonghe Liu, Steve Sheafar, Edward Knightly  
February 2004 IEEE/ACM Transactions on Networking (TON), Volume 12 Issue 1  
Publisher: IEEE Press

Full text available: Pdf (671.81 KB)

Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 32, Citation Count: 5

The Resilient Packet Ring (RPR) IEEE 802.17 standard is a new technology for high-speed backbone metropolitan area networks. A key performance objective of RPR is to simultaneously achieve high utilization, spatial reuse, and fairness, an objective not ...

Keywords: dynamic bandwidth allocation, fairness, metro rings, scheduling

## 17 A survey of peer-to-peer content distribution technologies



Stephanos Androutsellis-Theotokis, Diomidis Spinellis  
December 2004 ACM Computing Surveys (CSUR), Volume 36 Issue 4  
Publisher: ACM

Full text available: Pdf (517.77 KB)

Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 187, Downloads (12 Months): 2221, Citation Count: 42

Distributed computer architectures labeled "peer-to-peer" are designed for the sharing of computer resources (content, storage, CPU cycles) by direct exchange, rather than requiring the intermediation or support of a centralized server or authority. ...

Keywords: Content distribution, DHT, DOLR, grid computing, p2p, peer-to-peer

## 18 Software thread integration for embedded system display applications



Alexander G. Dean  
February 2006 ACM Transactions on Embedded Computing Systems (TECS), Volume 5 Issue 1  
Publisher: ACM

Full text available: Pdf (1.40 MB)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 176, Citation Count: 0

Embedded systems require control of many concurrent real-time activities, leading to system designs that feature a variety of hardware peripherals, with each providing a specific, dedicated service. These peripherals increase system size, cost, weight, ...

**Keywords:** fine-grain concurrency, hardware to software migration, software thread integration

## 19 Switching using parallel input-output queued switches with no speedup

Saad Mneimneh, Vishal Sharma, Kai-Yeung Siu

October 2002 IEEE/ ACM Transactions on Networking (TON), Volume 10 Issue 5

Publisher: IEEE Press

Full text available:  Pdf (354.89 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 24, Citation Count: 1

We propose an efficient parallel switching architecture that requires no speedup and guarantees bounded delay. Our architecture consists of  $k$  input-output-queued switches with first-in-first-out queues, operating at the line speed in parallel ...

**Keywords:** delay guarantee, parallel switches, speedup, switching

## 20 Software-directed power-aware interconnection networks



Vassos Soteriou, Noel Eisle, Li-Shiuan Peh

March 2007 ACM Transactions on Architecture and Code Optimization (TACO), Volume 4 Issue 1

Publisher: ACM

Full text available:  Pdf (966.00 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 16, Downloads (12 Months): 197, Citation Count: 0

Interconnection networks have been deployed as the communication fabric in a wide spectrum of parallel computer systems, ranging from chip multiprocessors (CMPs) and embedded multicore systems-on-a-chip (SoCs) to clusters and server blades. Recent technology ...




**Keywords:** Software-directed power reduction, communication links, dynamic voltage scaling, interconnection networks, on-chip networks, simulation

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